EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	201	703/15.ccor.	US-PGPUB; USPAT	OR	ON	2006/10/13 16:20
S2	641	703/14.ccor.	US-PGPUB; USPAT	OR	ON	2006/10/13 13:02
S3	397	703/13.ccor.	US-PGPUB; USPAT	OR	ON	2006/10/13 13:02
S4	103	703/18.ccor.	US-PGPUB; USPAT	OR	ON	2006/10/13 13:03
S5	128	local adj clock adj buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 14:28
S6	334168	power near3 consumption	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 14:28
S7	61	S5 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 14:29
S8	26	S7 and capacitance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 14:30
S9	5707	hardware adj descript$3 adj language	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:35
S10	2	(S9 hdl) and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:36
S11	3615	energy near3 model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:37
S12	5	S5 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:38
S13	209	S6 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:39
S14	72	S13 and capacitance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:40
S15	43	S14 and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:43

EAST Search History

S16	21	S15 and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:44
S17	18	S16 and @ad<="20040116"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/13 15:45
S18	8	("5655109" | "5696694" | "5805459" | "5838579" | "5903476" | "5949689" | "6075932" | "6195630").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/13 15:46
S19	21	("6345379").URPN.	USPAT	OR	ON	2006/10/13 15:47
S20	9	("5508937" | "5740067" | "5764525" | "6272667" | "6272668" | "6286128" | "6336205" | "6425110" | "6440780").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/13 16:55
S21	1	("6922818").URPN.	USPAT	OR	ON	2006/10/13 16:55

		Results
7.	((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(energy model)) and power consumption) and clock *[All Sources(- All Sciences -)]*	4
6.	((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(energy model)) and power consumption) and capacitance *[All Sources(- All Sciences -)]*	4
5.	(pub-date > 1959 and pub-date < 2005 and FULL-TEXT(energy model)) and power consumption *[All Sources(- All Sciences -)]*	45
4.	pub-date > 1959 and pub-date < 2005 and FULL-TEXT(energy model) *[All Sources(- All Sciences -)]*	7046
3.	pub-date > 1959 and pub-date < 2005 and FULL-TEXT(local clock buffer) *[All Sources(- All Sciences -)]*	0
2.	(pub-date > 1959 and pub-date < 2005 and FULL-TEXT(power consumption) and FULL-TEXT(clock buffer)) and capacitance *[All Sources(- All Sciences -)]*	6
1.	pub-date > 1959 and pub-date < 2005 and FULL-TEXT(power consumption) and FULL-TEXT(clock buffer) *[All Sources(- All Sciences -)]*	10

Fri, 13 Oct 2006, 6:45:12 PM EST

Edit an existing query or compose a new query in the Search Query Display.

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Search Query Display

[redacted]

Recent Search Queries

		Results
#1	(clock buffer) <and> (pyr >= 1951 <and> pyr <= 2004)	797
#2	((clock buffer<and>power consumption)) <and> (pyr >= 1951 <and> pyr <= 2004)	302
#3	((clock buffer<and>power consumption)<and>capacitance) <and> (pyr >= 1951 <and> pyr <= 2004)	200
#4	((clock buffer<and>power consumption) <and>capacitance<and>(energy model)) <and> (pyr >= 1951 <and> pyr <= 2004)	2
#5	((clock buffer<and>power consumption) <and>capacitance<and>energy<and>model) <and> (pyr >= 1951 <and> pyr <= 2004)	50
#6	((local clock buffer<and>power consumption) <and>capacitance<and>energy<and>model) <and> (pyr >= 1951 <and> pyr <= 2004)	7

[redacted]

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Searching for **clock buffer and power consumption and capacitance**.
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3 documents found. **Order: number of citations.**

Comparative Analysis of Master-Slave Latches and.. - Stojanovic, Oklobdzija (1999) (Correct) (15 citations)
portion of the power dissipated in the local **clock buffer** driving the clock input of the latch local
reveals the sources of performance and **power-consumption** bottlenecks in different design styles.
of node (for railto -rail swing)total **capacitance** of node clock frequency rail-to-rail
www.stanford.edu/class/ee371/handouts/stojanovic99.pdf

Single-Chip Cmos Optical Microspectrometer - Correia De Graaf (2000) (Correct) (1 citation)
Bus lines line driver line receiver Clock **clock buffer** Manchester Decoder Address Register
a microcontroller or a personal computer. **Power consumption** is 1200 W for a clock frequency of 1 MHz.
the charge across the integrating junction **capacitance**, thereby modulating the output frequency [8]
www.dei.uminho.pt/pessoas/higino/pampus/1trans99.pdf

Figure 24.5.7: The measured sensitivity of VCO frequency to .. - Figure Measured And (Correct)
2003 IEEE Figure 24.5.5: PLL and **clock buffer** die photograph.
simplicity and drive capability with low **power consumption**. However, CMOS inverters have poor supply
delay overhead are 30% and 18% due to parasitic **capacitance**. The area overhead is 50%Acknowledgements
www.ee.ucla.edu/faculty/bios/../papers/yang_isscc03.pdf

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